

# MAK 1 9 2007

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10826475	Docket Number	CNTR.2223
Filed	4/16/2004	Group Art Unit	2135
Examiner	KIM Y. VU	Customer No.	23669
Application Title	MICROPROCESSOR APPARATUS AND METHOD FOR PROVIDING CONFIGURABLE CRYPTOGRAPHIC KEY SIZE		
First Named Inventor	G. GLENN HENRY		

FACSIMILE COVER SHEET				
To: Fax Number:	Commissioner for Patents - Central Fax Number 571-273-8300			
From: Fax Number:	HUFFMAN PATENT GROUP, LLC (661) 460-1986			
Pages:	37 (including this cover sheet)			
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Please accept the attached correspondence for the above-identified matter. I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.				

	Respectfully submitted, HUFFMAN PATENT GROUP, LLC				
/Richard K. Huffman/					
Ву:					
	RICHARD K. HUFFMAN, P.E. Registration No. 41,082 Tel: (719) 575-9998				
	03/18/2001				
Date:					

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Filed	4/16/2004	Group Art Unit	2135	
Examiner	KIM Y. VU	Customer No.	23669	
Application Title	MICROPROCESSOR APPARATUS AND METHOD FOR PROVIDING CONFIGURABLE CRYPTOGRAPHIC KEY SIZE			
First Named Inventor	G. GLENN HENRY			

### INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

VIA FACSIMILE: 571-273-8300

Mail Stop **AMENDMENT** Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 submitted under 37 CFR 1.97(b) listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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Respectfully submitted,

/ Richard K. Hullman/

By:

RICHARD K. HUFFMAN, P.E.

Registration No. 41,082

Tel: (719) 575-9998

03/18/2001

Date:

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	U.S. PATENT DOCUMENTS						
Examiner	Cite	Cite Document Number No. 1 Number-Kind Code 2(# known)	Publication Date MM-DD-YYYY	Name of Patentee or	Pages, Columns, Lines, Where		
Initials*				Applicant of Cited Document	Relevant Passages or Relevant Figure Appear		
		4168396 .	09-18-1979	Robert M. Best			
<u> </u>		4278837	07-14-1981	Robert M. Best			
		4465901	08-14-1984	Robert M. Best			
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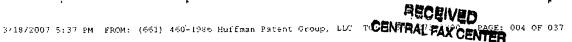
	FOREIGN PATENT DOCUMENTS							
Examiner	Cite Foreign Patent Document Publication Date Name of Patentee or Applicant of Cited Document  No. Country Code Number - Kind Code MM-DD-YYYY Applicant of Cited Document	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T-6		
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	<b>T</b> 2
	AA	Dimond et al. "Application-specific Customisation of Multi-Threaded Soft Processors." Computers and Digital Techniques.  IEEE Proceedings, Volume 153, Issue 3, 2 May 2006, Pages 173-180.	
	AB	Anderson et al. "Cryptographic Processors - A Survey." Proceedings of the IEEE. Volume 94, Issue 2. Feb. 2006. Pages 357-369.	
	AC	Eberle et al. "Architectural Extensions for Elliptic Curve Cryptography over GF(2 <sup>th</sup> ) on 8-bit Microprocessors." Application- Specific Systems. Architecture Processors. 2005. ASAP 2005. 16th IEEE International Conference on 23-25 July 2005. Pages 343-349.	
<del></del>	AD	KUHN, Markus G. "Cipher Instruction Search Attack on the Bus-Encryption Security Microcontroller DS5002FP." Computers, IEEE Transactions on Volume 47, Issue 10. Oct. 1998. Pages 1153-1157.	
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Applicant's unique citation designation number (optional). 'Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450